

	Application No.	Applicant(s)	Applicant(s)	
Notice of Allowability	10/707,562	HSU ET AL.		
	Examiner	Art Unit		
	David Lam	2827		
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate comm IGHTS. This application is	n this application. If not includ unication will be mailed in due	ed course. <b>THIS</b>	
1. This communication is responsive to				
2. The allowed claim(s) is/are 1-16.				
3. $\boxtimes$ The drawings filed on <u>22 December 2003</u> are accepted by	the Examiner.			
4.	e been received.  e been received in Application cuments have been received  of this communication to file fENT of this application.  hitted. Note the attached EX es reason(s) why the oath of st be submitted.  son's Patent Drawing Review  s Amendment / Comment of  .84(c)) should be written on the header according to 37 Cl  sit of BIOLOGICAL MAT	on No  ed in this national stage applicate a reply complying with the resonable are a reply complying with the	quirements NOTICE OF	
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview S Paper No. 7. Examiner's	Informal Patent Application (PT Summary (PTO-413),  I/Mail Date  S Amendment/Comment  S Statement of Reasons for Alle	, and the second	
		DAVID LAM PRIMARY EXAM		

## Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance: Claims 1-16 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: method for writing a memory module comprising step of applying a word line voltage to a word line connected to memory cell to be written, to inject hot electrons on a junction between the substrate and first p-type doped region of the memory cell to be written by band-to-band tunneling injection, and among other steps as claimed in independent claim 1; method for reading a memory module comprising step of applying a word line voltage to the remaining word lines not connected to the memory cell to be read, to enlarge a depletion region between a second p-type doped region and the substrate of the memory cell to be read, and among other steps as claimed in independent claim 6; method for erasing a memory module comprising step of applying a voltage equal to first transmission line voltage to the substrates of the plurality of memory cells, and among other steps as claimed in independent claim 12.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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- Hirono (6,735,125) discloses a nonvolatile semiconductor memory device and method of erasing the same.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

DAVID LAM
PRIMARY EXAMINER